IN THE CLAIMS:

1. (Currently Amended) A plasma display panel comprising:

a ~~first~~ scan electrode and a ~~second~~ sustain electrode ~~which~~ that are disposed in parallel with each other on a ~~first~~ front substrate;

a ~~third~~ data electrode disposed on a ~~second~~ back substrate in a direction orthogonal to the ~~first~~ scan electrode and the ~~second~~ sustain electrode, the ~~second~~ back substrate being disposed to face the ~~first~~ front substrate with a discharge space therebetween;

~~a fourth electrode disposed on the second substrate in such a manner as to be parallel with the first electrode and the second electrode;~~ and

a first discharge space and a second discharge space ~~which~~ that are formed ~~on~~ between the ~~second~~ front substrate and the back substrate by being partitioned by a barrier rib, wherein

a main discharge cell for performing a discharge with the ~~first~~ scan electrode, the ~~second~~ sustain electrode and the ~~third~~ data electrode is formed in the first discharge space, a dielectric layer is formed on the back substrate in the second

discharge space, a priming electrode is disposed on the dielectric layer in a manner to make the priming electrode parallel to the scan electrode and the sustain electrode, and a priming discharge cell for performing a discharge with the ~~fourth~~ scan electrode ~~and at least one of the first electrode~~ and the ~~second~~ priming electrode is formed in the second discharge space~~, and~~

~~in the second discharge space, the fourth electrode is formed on a dielectric layer and is disposed closer to the first electrode and the second electrode than the third electrode~~.

2. (Canceled)

3. (Currently Amended) The plasma display panel according to claim 1, wherein

the barrier rib is formed of a longitudinal rib part extending in the direction orthogonal to the ~~first~~ scan electrode and the ~~second~~ sustain electrode, and a lateral rib part for forming a gap part continuous in parallel with the ~~first~~ scan electrode and the ~~second~~ sustain electrode, and

the gap part forms the second discharge space.

4. (Currently Amended) A method for manufacturing a plasma display panel, comprising the steps of:

forming a main discharge cell in a first discharge space, the main discharge cell including:

a ~~first~~ scan electrode and a ~~second~~ sustain electrode which that are disposed in parallel with each other on a ~~first~~ front substrate;

a ~~third~~ data electrode disposed on a ~~second~~ back substrate in a direction orthogonal to the ~~first~~ scan electrode and the ~~second~~ sustain electrode, the ~~second~~ back substrate being disposed to face the ~~first~~ front substrate with a discharge space therebetween;

~~a fourth electrode disposed on the second substrate in such a manner as to be parallel with the first electrode and the second electrode; and~~

the first discharge space and a second discharge space ~~which~~ that are formed ~~on~~ between the front substrate and the ~~second~~ back substrate by being partitioned by a barrier rib, and the main discharge cell performing a discharge with the ~~first~~ scan electrode, the ~~second~~ sustain electrode and the ~~third~~

data electrode; ~~and~~

forming a dielectric layer that is formed on the back substrate in the second discharge space;

forming a priming electrode on the dielectric layer in a manner to make the primary electrode parallel to the scan electrode and the sustain electrode; and

forming a priming discharge cell in the second discharge space, the priming discharge cell performing a discharge with the ~~fourth~~ priming electrode and ~~at least one of~~ the ~~first~~ scan electrode ~~and the second electrode~~, wherein

the step of forming the second discharge space includes the steps of:

forming ~~a~~ the dielectric layer continuous in a longitudinal direction orthogonal at least to the ~~third~~ data electrode; and

forming the ~~fourth~~ priming electrode continuous on the dielectric layer.

5. (Original) The method for manufacturing the plasma display panel according to claim 4, wherein

the step of forming the dielectric layer includes the step of filling dielectric paste into the second discharge space by

discharging the dielectric paste at least through a nozzle.

6. (Currently Amended) The method for manufacturing the plasma display panel according to claim 4, wherein

the step of forming the ~~fourth~~ priming electrode includes the step of filling electrode material paste into the second discharge space by discharging the electrode material paste at least through a nozzle.

7. (Currently Amended) The method for manufacturing the plasma display panel according to claim 5 further comprising the step of continuously filling the dielectric layer after the barrier rib is patterned on the ~~second~~ back substrate.

8. (Original) The method for manufacturing the plasma display panel according to claim 7, wherein

the barrier rib and the dielectric layer concurrently undergo firing and solidification.